

Microsoft Research PhD Summer School 2012

Poster Session Tuesday, 3rd July

Student Name	Affiliation	Poster Title
Aapo Kalliola	Aalto University	Improving the DDoS resistance of Internet services
Anja Thieme	Newcastle University	Supporting Mental Wellbeing through Digital Design
Chandrika Cyclic	Brunel University	Interaction in the family car: Managing conversations & Media
Mahmoud Awad	Queen's University of Belfast	Microsoft AGE: Adapted Games for Elderly
Oliver Bates	Lancaster University	Towards activity-relevant attribution of IT energy usage
Paul Kelly	University of Oxford	Public Health Applications for SenseCam in the field of Travel Research
Simo Hosio	University of Oulu	Social Networking Services for Public Spaces
Chloe Brown	University of Cambridge	Local Communities in Online Social Networks
Mohamed Ibrahim	Egypt-Japan University of Science and Technology	CogFrame: Enabling Large Scale Flexible Deployment of Cognitive Radio Routing Protocols
Natalia Prytkova	Max Planck Institute for Computer Science	Predicting the Evolution of Collective Web Catalogues
Alexander Okoledov	MIPT	Wake Up Studio - Makes your Good Morning
Valentina Kustikova	Nizhny Novgorod State University	Development of control system to detect incorrect pupil's posture using Kinect
Alan Mujumdar	University of Cambridge	Communication In Massively Parallel Cloud Computer Systems
Andrius Aucinas	University of Cambridge	ThinkAir: the power of mobile cloud computing
Matthew P. Grosvenor	University of Cambridge	uvNIC: Rapid Prototyping NIC Device Drivers
Ahmad Showail	KAUST	Optimal Buffer Size for Wireless Mesh Networks
Daniel Nemirovsky	Barcelona Supercomputing Centre	Control-Data Decoupled Architecture
Denis Efremov	The Institute for System Programming of the Russian Academy of Sciences	Sevigator - Virtualization-based tool for preventing sensitive data leaks from a computer running untrusted commodity operating system
Nihal Dindar	ETH Zurich	DejaVu: A Complex Event Processing System for Pattern Matching over Live and Historical Data Streams
Tassadaq Hussain	University Politechnique Catalonia	Programmable Memory Controller for Vector System-on-Chip
Robert Norton	University of Cambridge	Exploring the Hardware-Software Interface using an FPGA-based Research Platform
Tyler Crain	University of Rennes	A Universal Construction for transaction based multi-process programs
Karthik Nilakant	University of Cambridge	Towards A Next Generation Data-Parallel Platform For Graph Processing